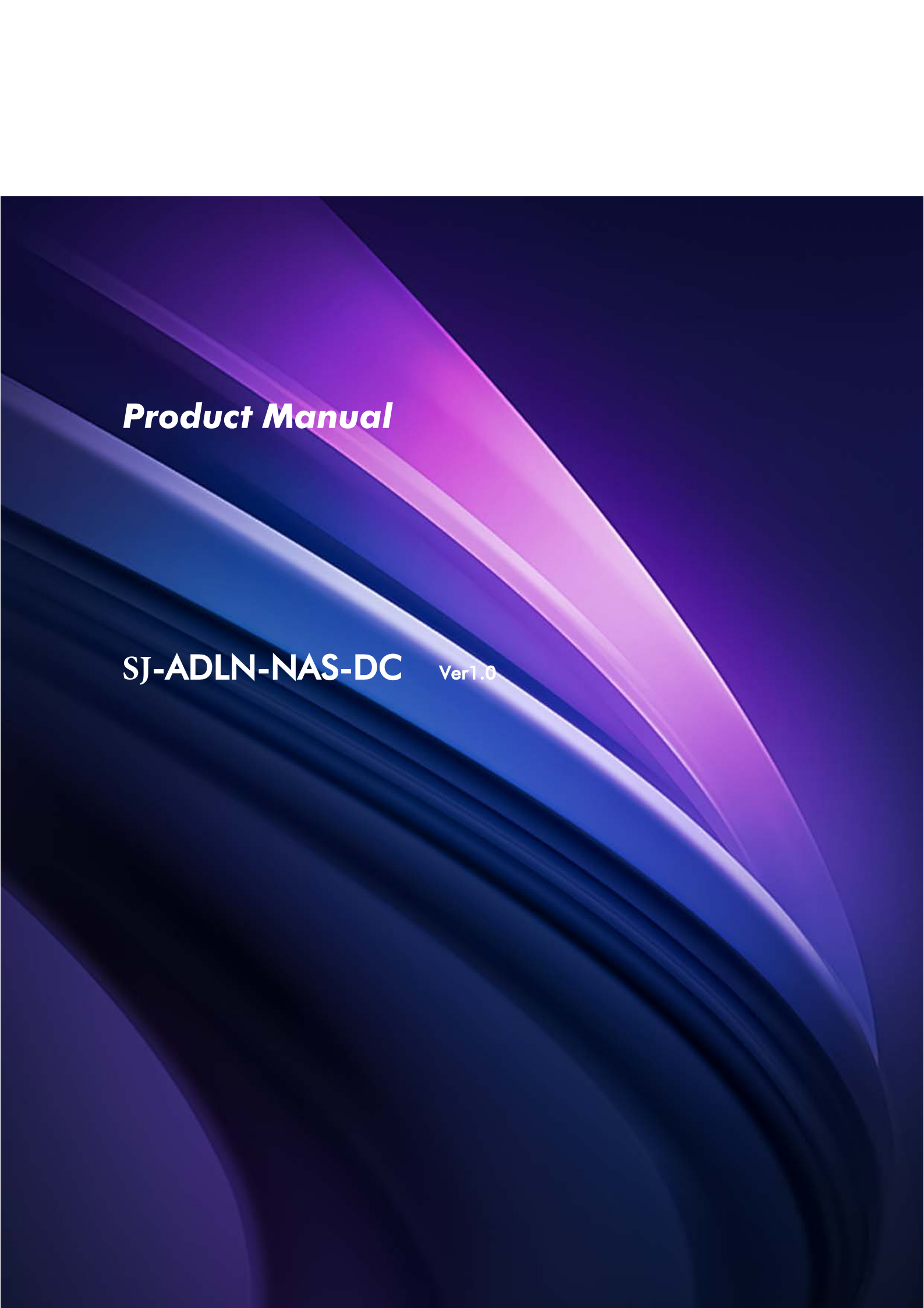




Product Manual

SJ-ADLN-NAS-DC Ver1.0

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Manual' s first edition:

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Version	Date	Description
V1.0	2023-12-28	Initial Release

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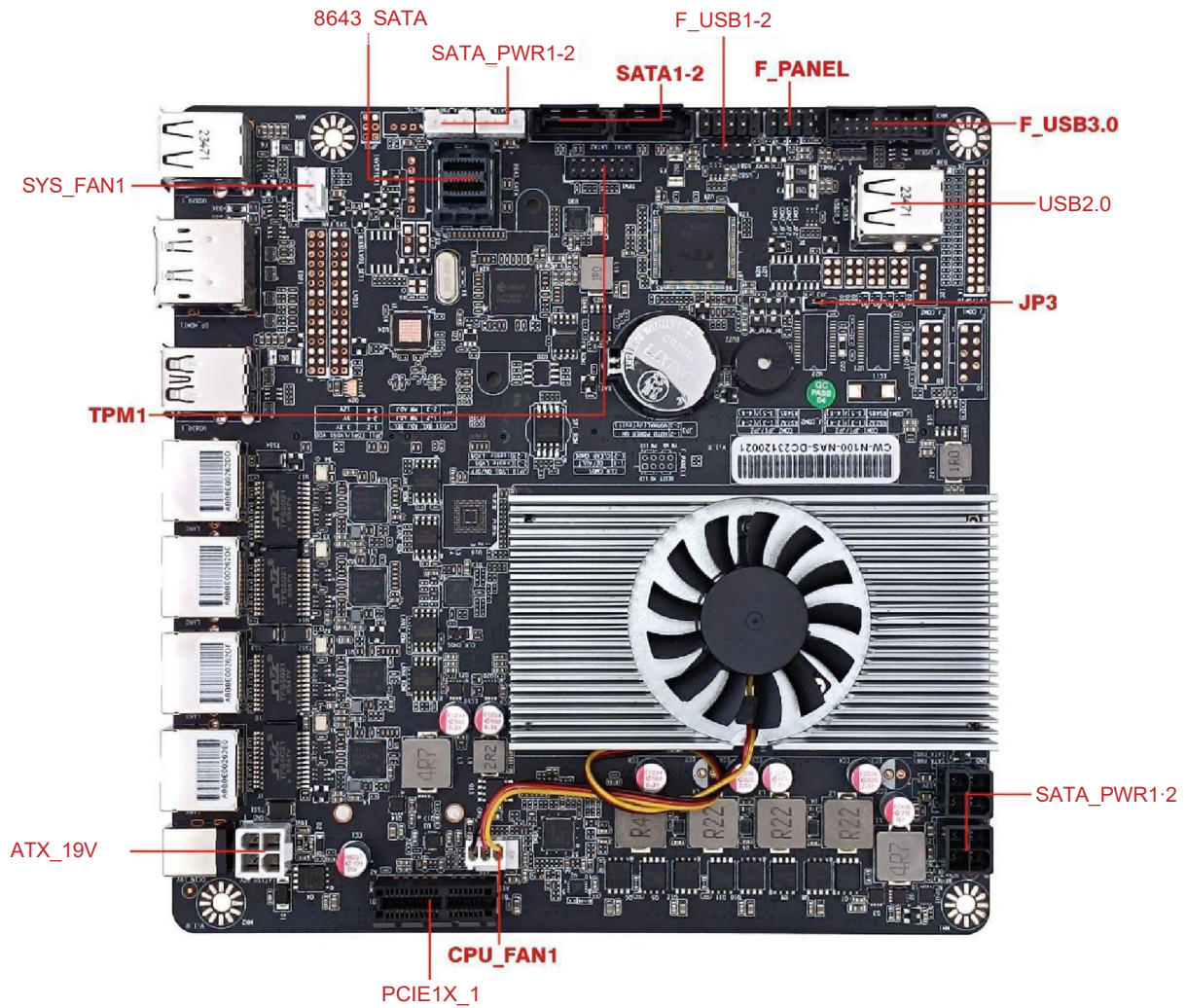
Chapter 1. Introduction

1.1 Motherboard Specifications

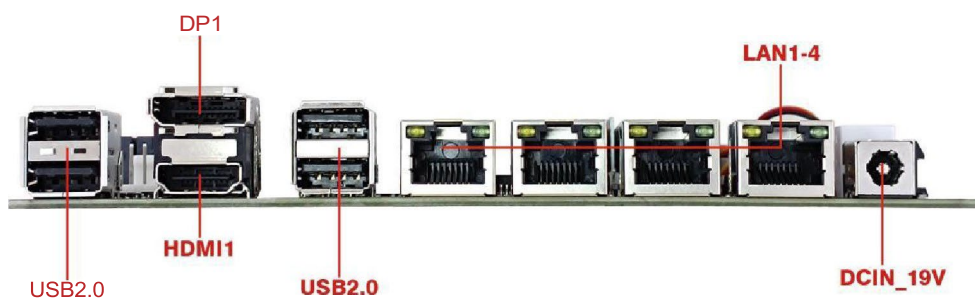
Motherboard size	- 170mmX170mm
CPU	- Intel® Alder Lake-N CPU
Memory	- Onboard 1 262-pin DDR5 SO-DIMM memory slot - Single memory supports up to 16GB
Expansion slots	- 1* M2_NVME; 1*M.2_SSD; 1*M.2_WIFI; 1*PCIE1X
Backplane interface	- 1 x DCIN interface - 4 x LAN interface - 1 x HDMI interface - 1 x DP interface - 4 x USB2.0 interface
Built-in interface	- 2 x F_USB1 Pin - 1 x F_USB3.0 Pin - 1*8643_SATA Pin - 2*USB2.0 Pin - 1 x TPM Pin - 2 x SATA_PWR Pin - 2 x SATA interface - 1 x F_PANEL Pin - 1 x CPU_FAN Socket - 1 x SYS_FAN Socket - 1 x PCIE1X Socket - 1 x JP3 Jumper cap
BIOS	- AMI BIOS
Power management	- Auto Power On
Display	- Suppoert HDMI DP
Network	- 4 x Intel I226-V NIC (transfer rate up to 2.5G)
Power	- PWIN_19V / DC 19V power supply
Working environment	- Working temperature: -20°C~60°C - Ambient humidity: 0%~90%
Operating system	Windows 10 / Windows 11

1.2 Mainboard layout diagram

CW-ADLN-NAS-DC V1.0



170*170

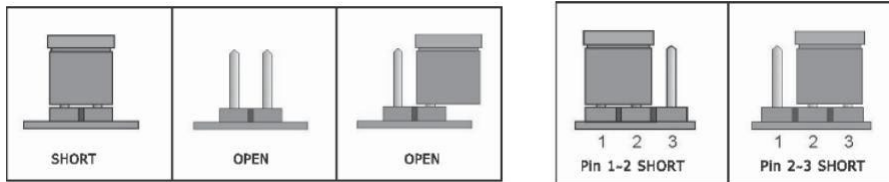


Chapter 2. Definitions

2.1 Jumper Description

2-pin connector: Inserting a jumper cap across both pins will close (short-circuit).

3-pin connector: A jumper cap can be inserted across pins 1-2 or pins 2-3 to close (short-circuit).



How to identify the position of pin 1 of the jumper?

Please check the motherboard carefully. The pin marked with "1" or with a thick white line is pin 1.

2.2, Definition of each pin

1. F_USB1

	PIN	Define	PIN	Define
	1	5V	2	5V
	3	USB1-	4	USB2-
	5	USB1+	6	USB2+
	7	GND	8	GND
	9		10	GND

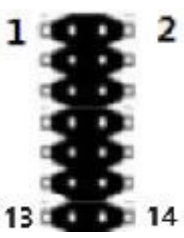
2.SATA_PWR

	PIN	Define
	1	5V
	2	GND
	3	GND
	4	12V

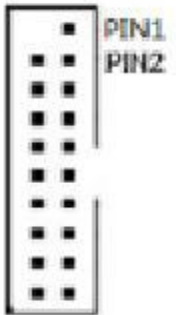
3.F_PANEL

	STATUS	Setting
	1-3	HD_LED
	2-4	PW_LED
	5-7	Reset
	6-8	PW_BN

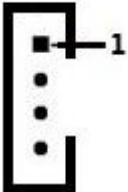
4. TPM

	PIN	Define	PIN	Define
	1	3.3V	2	SPI_IRQ
	3	PLTRST	4	SPI_CS
	5	NA	6	SPI_WP
	7	3.3V	8	GND
	9	NA	10	SPI_CLOCK
	11	SPI_MISO	12	SPI_MOSI
	13	SPI_HOLD	14	

5. F_USB3.0

	PIN	Define	PIN	Define
	1	Vbus	11	N/A
	2	IntA_P1_SSRX-	12	Vbus
	3	IntA_P1_SSRX+	13	IntA_P2_SSRX-
	4	GND	14	IntA_P2_SSRX+
	5	IntA_P1_SSTX-	15	GND
	6	IntA_P1_SSTX+	16	IntA_P2_SSTX-
	7	GND	17	IntA_P2_SSTX+
	8	IntA_P1_D-	18	GND
	9	IntA_P1_D+	19	IntA_P2_D-
	10	ID	20	IntA_P2_D+

6. CPU_FAN

	PIN	Define
	1	GND
	2	12V
	3	DET
	4	PWM

7.JP3

 A diagram of a 3-pin jumper header. The pins are numbered 1, 2, and 3 from top to bottom. Pin 1 is the top pin, pin 2 is the middle pin, and pin 3 is the bottom pin.	STATUS	Setting
	1-2	AUTO POWER ON
	2-3	NORMAL(default)

8.SATA_PWR (Black seat)

 A diagram of a 4-pin SATA power connector. The pins are numbered 1, 2, 3, and 4 from left to right. Pin 1 is the top-left pin, pin 2 is the top-right pin, pin 3 is the bottom-left pin, and pin 4 is the bottom-right pin.	PIN	Define
	1	GND
	2	GND
	3	5V
	4	+12V

9.ATXIN 19V

 A diagram of a 4-pin ATX power connector. The pins are numbered 1, 2, 3, and 4 from left to right. Pin 1 is the top-left pin, pin 2 is the top-right pin, pin 3 is the bottom-left pin, and pin 4 is the bottom-right pin.	PIN	Define
	1	GND
	2	GND
	3	19V
	4	19V

Chapter 3. BIOS Setup

3.1 BIOS Description

This motherboard uses AMI BIOS. BIOS stands for Basic Input Output System. It is a set of programs fixed to a ROM chip on the motherboard of the computer. It stores the most important basic input and output programs of the computer, the self-test program after power-on, and the system self-starting program. It can read and write specific information of system settings from CMOS. Its main function is to provide the lowest-level and most direct hardware settings and controls for the computer.

Note:

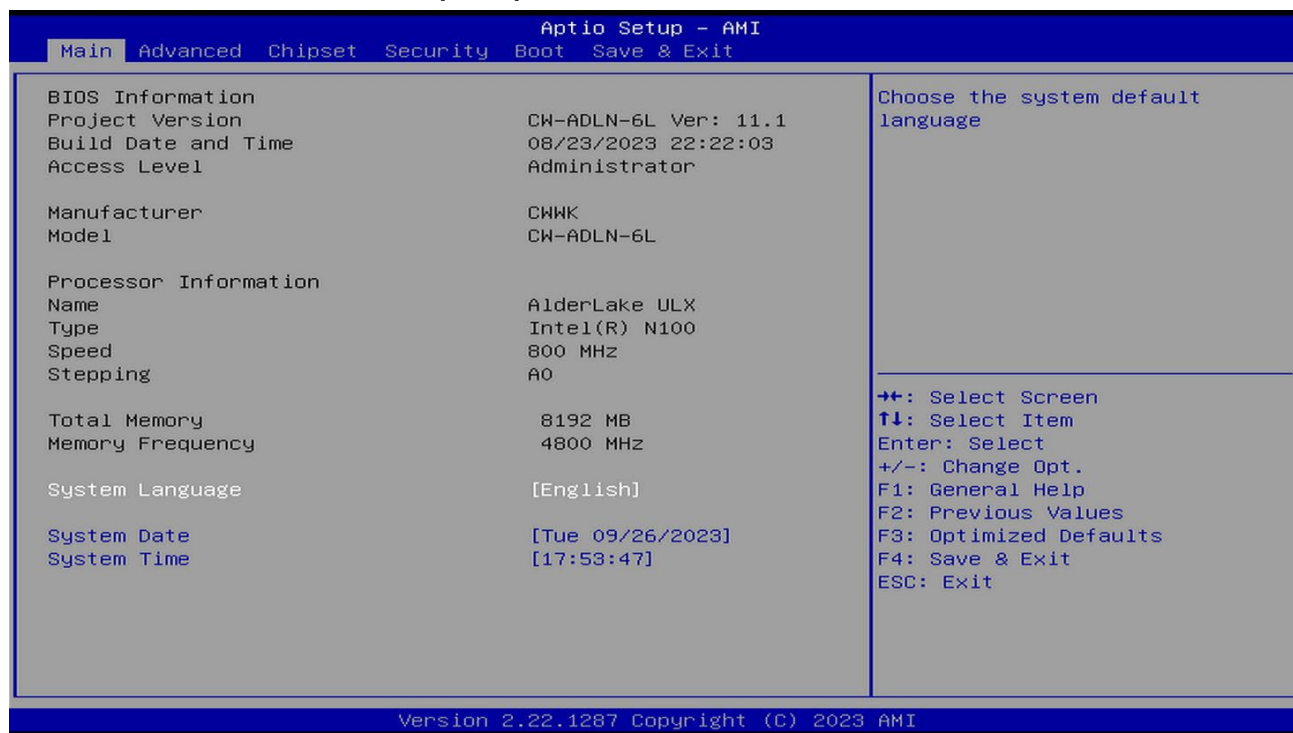
Due to the continuous upgrade of the BIOS in the motherboard, the relevant BIOS information in this manual is for reference only, so the consistency between the BIOS information in this manual and the actual BIOS information in the motherboard is not guaranteed.

Follow the steps below to enter the BIOS interface

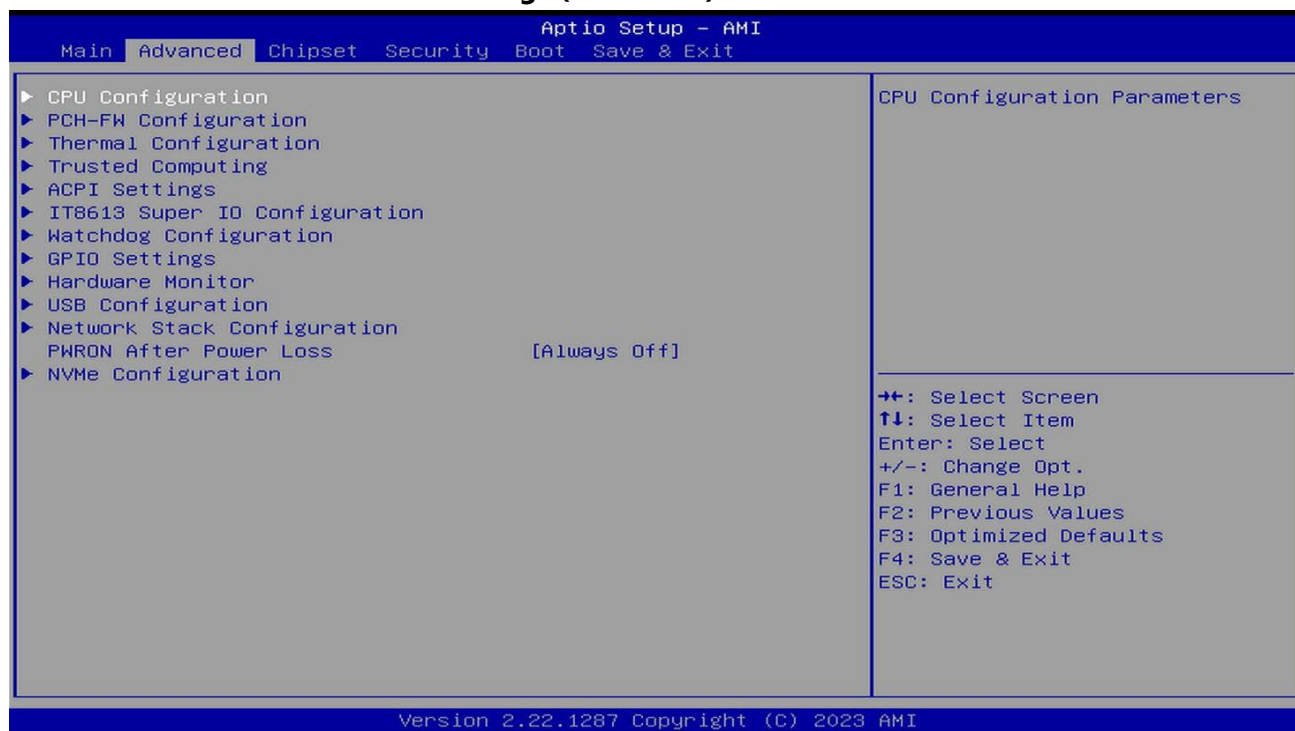
1. Turn on the power, the POST interface will appear on the monitor screen. Press the or <ESC> key to enter the BIOS setup program.
2. Use the arrow keys <↑><↓><←><→> to move the option you want to modify and press the <Enter> key to enter the sub-screen of the option.
3. Use the <Enter> key to modify the content of the selected item and press the Enter key to modify.
4. <Page Up/+> Increase the numeric value or change
<Page Down/-> Decrease the numeric value or change
<F1> Set submenu help
<F3> Set to default value (optimize to factory settings)
<F4> Save BIOS settings

3.2 BIOS settings

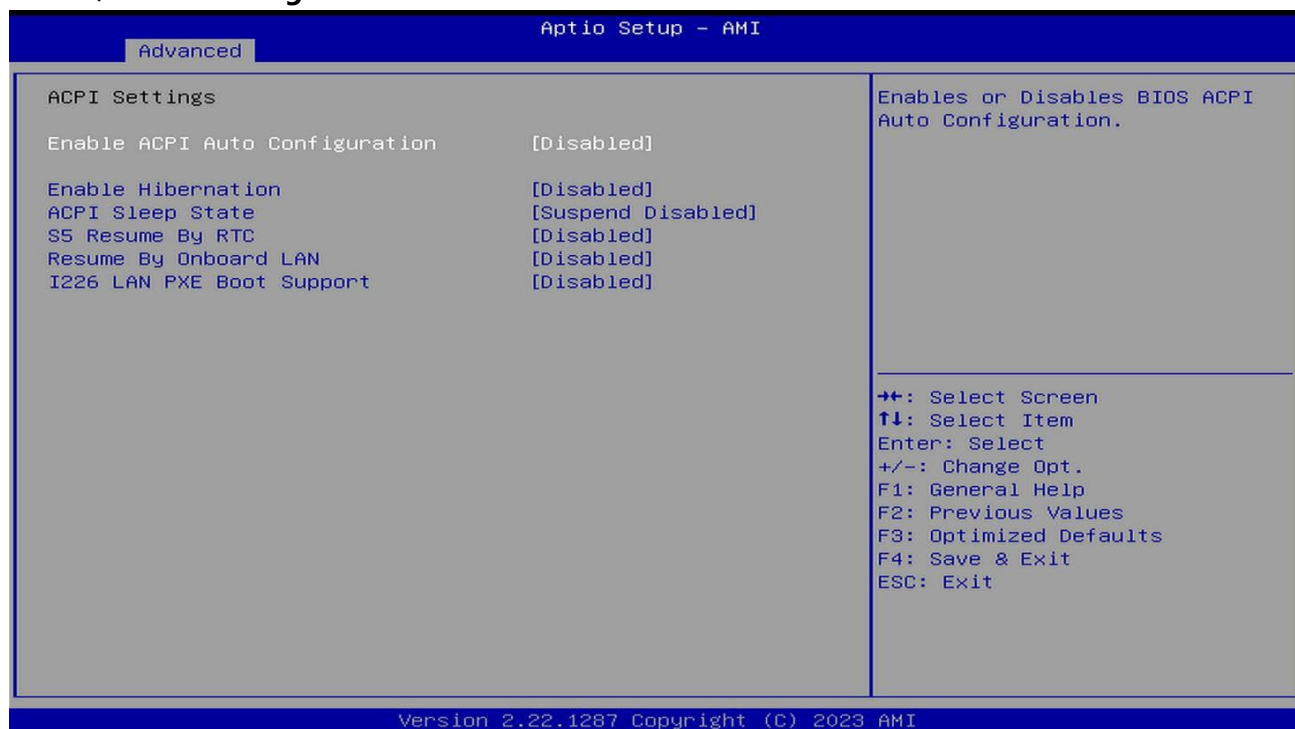
3.2.1 Main menu information (Main)



3.2.2 Advanced BIOS function settings (Advanced)



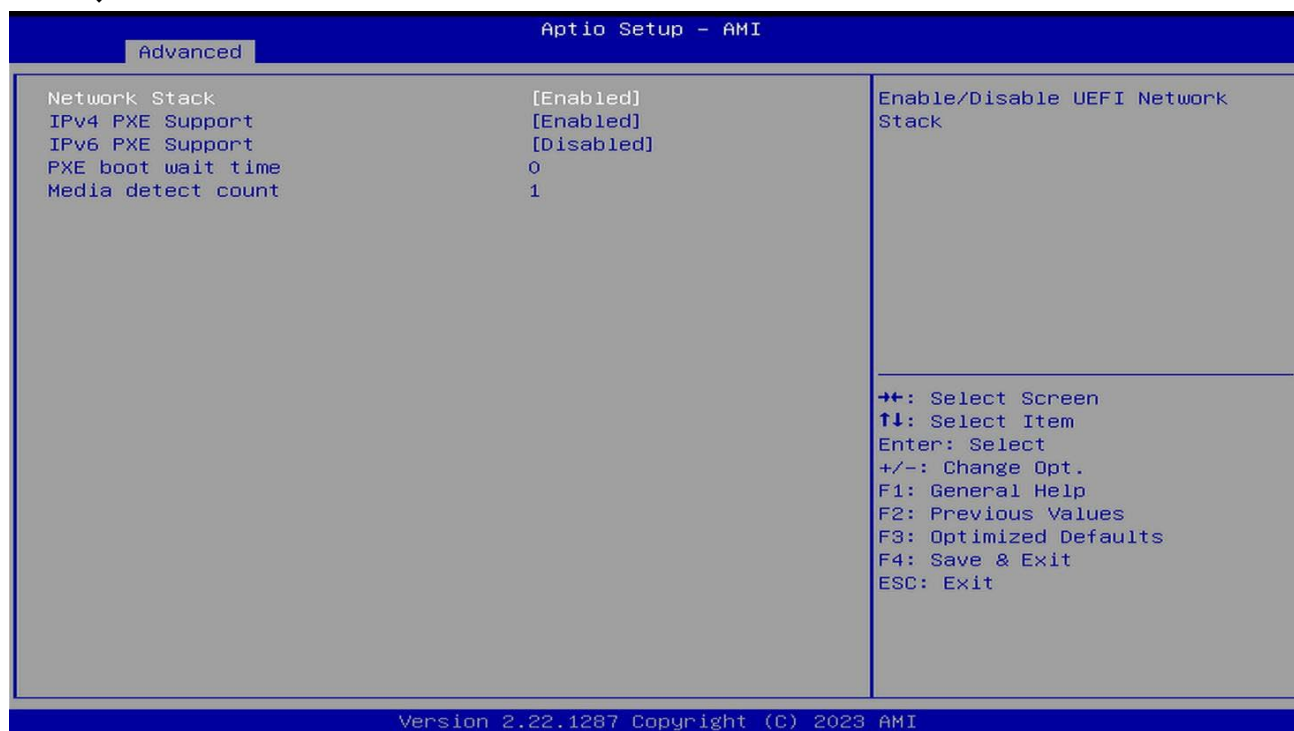
3.2.2. ACPI Settings



- **Resume By onboard LAN**

This option is used to set up Wake-on-LAN. (Not supported for S3/S4)

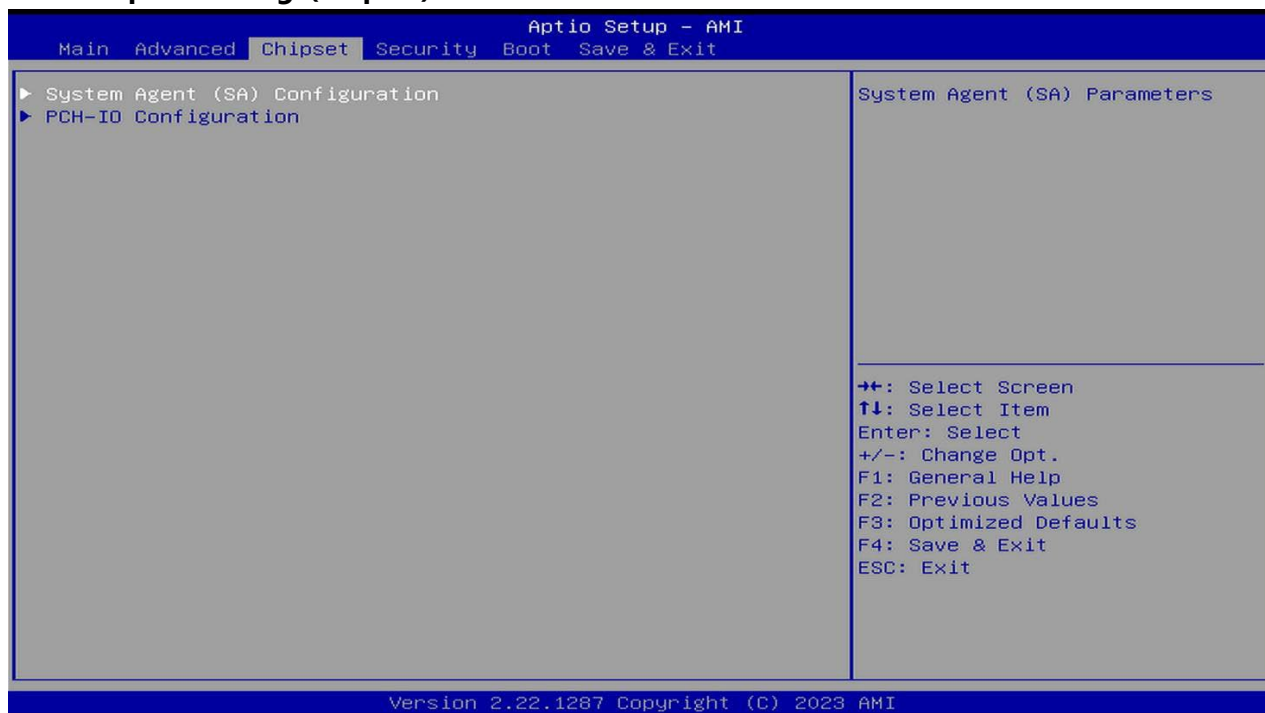
3.2.3、Network Stack



- **IPv4 PXE Support**

This option is used to set up network wake-up. (You need to enter ACPI Settings and set I226 LAN PXE Boot Support to Enabled)

3.2.4 Chipset Settings(Chipset)

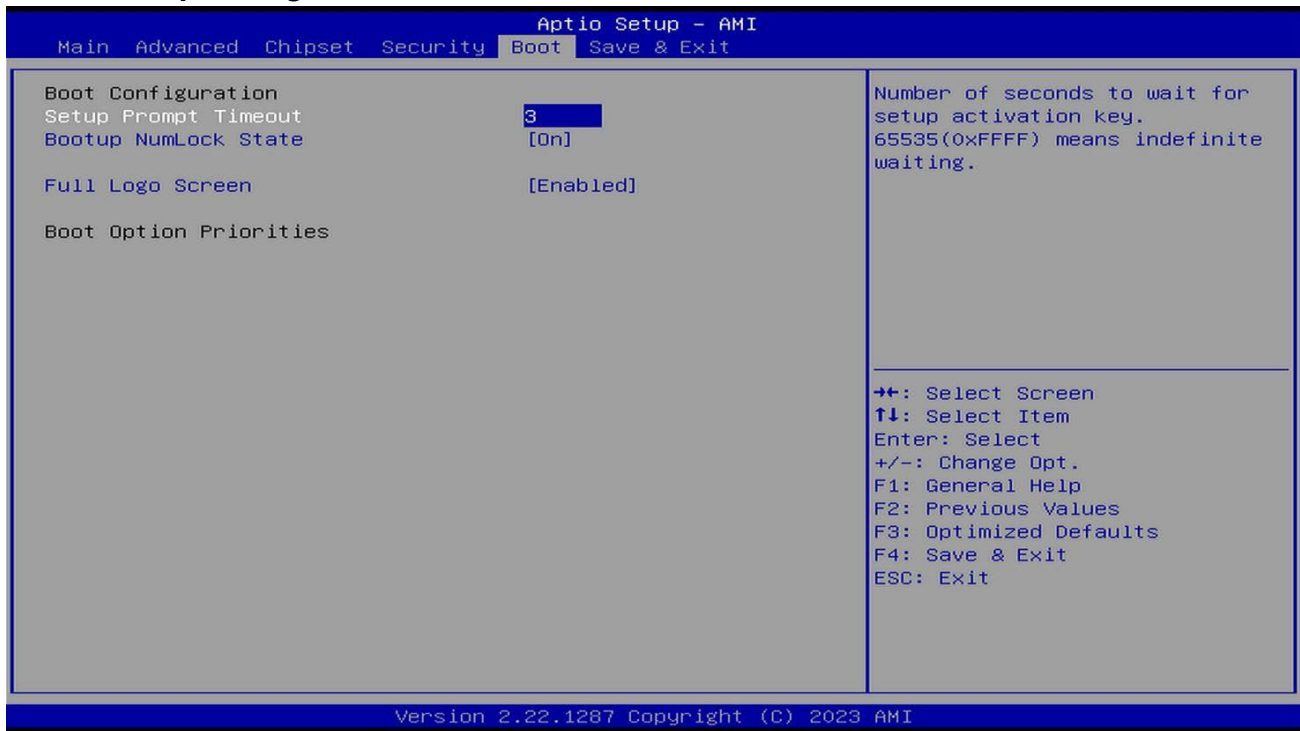


3.2.5 Security Settings(Security)

Aptio Setup - AMI									
Main	Advanced	Chipset	Security	Boot	Save & Exit				
Password Description If ONLY the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup. If ONLY the User's password is set, then this is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights. The password length must be in the following range: <table><tr><td>Minimum length</td><td>3</td></tr><tr><td>Maximum length</td><td>20</td></tr></table> Administrator Password User Password ► Secure Boot			Minimum length	3	Maximum length	20	Set Administrator Password →+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit		
Minimum length	3								
Maximum length	20								

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3.2.6 Startup Settings(Boot)



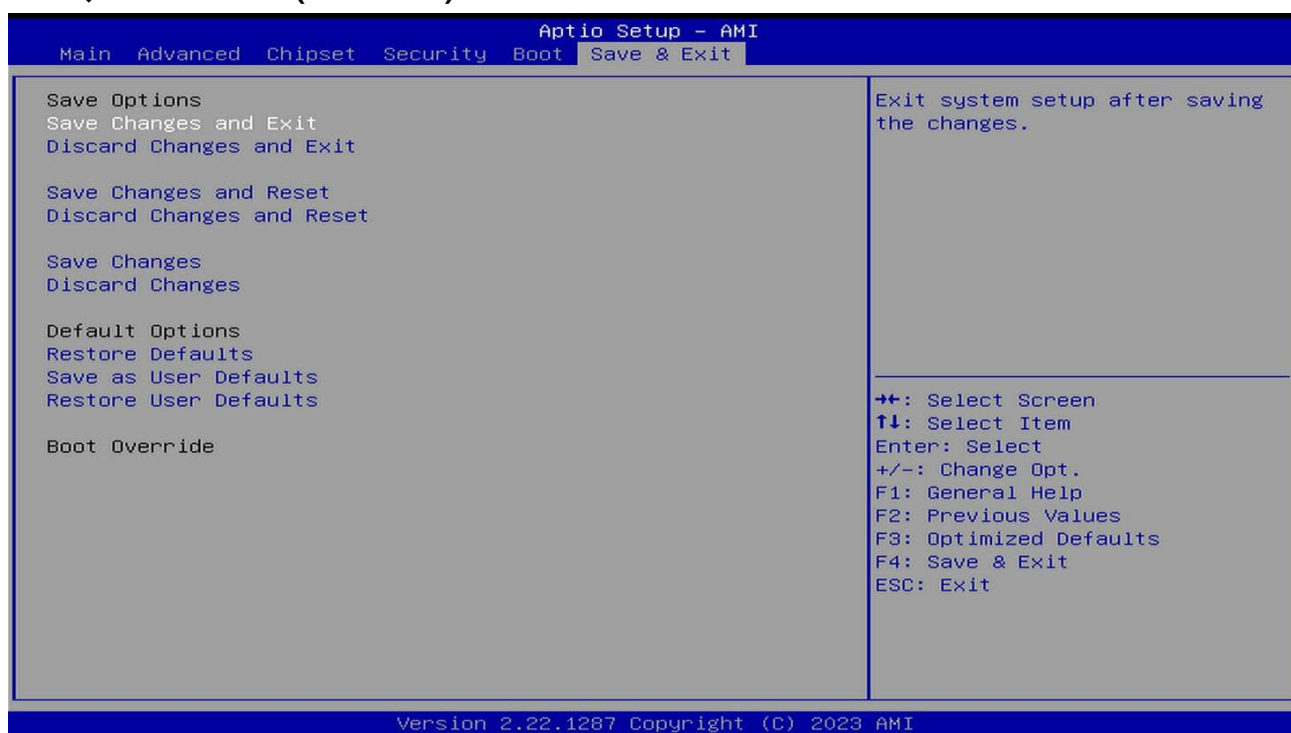
- **Setup Prompt Timeout**

This option is used to set the duration of the POST interface.

- **Bootup Numlock State**

Used to set the state of Numlock after the system starts. When set to On, Numlock will be turned on after the system starts, and the numeric keys on the keypad will be valid. When set to Off, Numlock will be turned off after the system starts, and the direction keys on the keypad will be valid.

3.2.7 、 Save and exit(Save&Exit)



- Save Changes and Exit
- Discard Changes and Exit
- Save Changes and Reset
- Discard Changes and Reset
- Save Changes
- Discard Changes
- Restore Defaults
- Save as User Defaults
- Restore User Defaults